



**Technology:**

- CMOS components for low power consumption and extensive use of Surface Mount components.
- Employing In-System-Programming technology for EPLD.

**Power Requirement:**

- Isolated unregulated 12VDC (1/2 A) or 24VDC (1/4 A).

**On-board Power Supplies:**

- Switching mode, +5 VDC for digital electronics, isolated -12 VDC and +12 VDC for analog electronics.

**System Status Indicators:**

- Scanning indicator.
- Power On indicator.

**Data and Address Bus:**

- 26-pin ribbon cable connection daisy-chained to a maximum of 14 AI/AO board combinations.

**Analog Inputs:**

- Number of Inputs: 32 Analog Input Points with one shield for every 2 points.
- Input Types: Bipolar, Differential input @ +/- 10.0V, +/- 5.0V, +/- 1.0V, +/- 1 mA, +/- 20 mA, 4-20 mA.
- Resolution: 15 bit plus sign.
- Temperature Coefficient: +/-5PPM per degree Celsius.
- Accuracy: +/- 0.05% (DC voltage input).
- Auto Self-Calibration Reference Points: -5.000 VDC, 0VDC and +5.000DC.
- CMRR (@0-60Hz): 90 dB.
- NMRR (@60Hz): 60 dB.
- Calibration: Analog input self-calibration, Auto-correction of gain and offset errors.

**Operating Environment:**

- Temperature Range: -40 to +85 degrees Celsius.
- Humidity: < 95%, non-condensing.

**Physical Dimensions:**

- 19.0 x 5.25 inches for 19" standard rack mount or panel mount.