



### Technology:

- CMOS components for low power consumption and extensive use of Surface Mount components.
- Employing In-System-Programming technology for EPLD.

### Power Requirement:

- Isolated unregulated 12VDC (1/2 A) or 24VDC (1/4 A) for electronics.
- Separate Power Supply for Digital Input Excitation.
- An additional option is to use an external source for Digital Input Excitation.

**On-board Power Supplies:** Switching mode, +5 VDC for digital electronics.

### System Status Indicators:

- Integrity check / Change of State indicator.
- Power On indicator.

### Data and Address Bus:

- 40-pin ribbon cable connection daisy-chained to a maximum of 14 DI/DO board combinations.

### Digital Inputs:

- Number of Inputs: 32 Optical Isolated Digital Inputs with Input indicator on each digital input.
- Isolation Rating: 1500 Vrms (Point Input to Logic).
- Hardware Digital Filter: Programmable 2-15ms debounce timer, transition detection and interrupt capability.
- Accumulator: Any digital input can be configured as an Accumulator.
- Maximum Rate of Accumulator: 150 Hz.
- Types of point: Form A, Form C and BCD.
- SOE: Any Digital Input Point can be Configured as SOE with accuracy of 1 ms.
- Chatter Filter: Available for preventing malfunctioning inputs from filling up the SOE buffer.

### Operating Environment:

- Temperature Range: -40 to +85 degrees Celsius
- Humidity: < 95%, non-condensing

### Physical Dimensions:

- 19.0 x 3.5 inches for 19" standard rack mount or panel mount.